In the Claims:

Please amend the claims as indicated.

1. (Currently amended) An apparatus for managing errors in prefetched data, the apparatus comprising:

a prefetch module configured to prefetch a data packet from a first location into a second location in anticipation of receiving a request for the prefetched data packet and prior to receiving the request;

a validation module configured to determine that [[a]]the prefetched data packet contains an uncorrectable error;

an identification module configured to associate an identifier with the prefetched data packet prior to receiving the request if the prefetched data packet contains the uncorrectable error;

a transfer module configured to transfer the prefetched data packet from the second location if the request is received; and

an error recovery module configured to selectively initiate an error recovery process for the transferred prefetched data packet that has been determined to contain an uncorrectable error if the identifier is associated with the prefetched data packet.

2. (Canceled)

3. (Currently amended) The apparatus of claim [[2]]1, wherein the identifier is stored in the second location with the prefetched data packet.

4. (Original) The apparatus of claim 1, wherein the validation module is further configured to store an address for the prefetched data packet within the first location.

5. (Original) The apparatus of claim 1, wherein the error recovery module is configured to set a flag in response to transfer of the prefetched data packet, and wherein the validation module is configured to signal an interrupt to initiate the error recovery process in response to the set flag.

6. (Currently amended) An apparatus for managing errors in prefetched data, the apparatus comprising:

a request module configured to request a transfer of a data packet from a first location by way of a communication bus;

a data transfer interface configured to prefetch the ~~requested~~ data packet from the first location into a second location in anticipation of receiving a request from the request module for the prefetched data packet and prior to receiving the request and transferring the data to the request module across the communication bus, the data transfer interface further configured to determine that [[a]]the prefetched data packet contains an uncorrectable error, associate an identifier with the prefetched data packet prior to receiving the request if the prefetched data packet contains the

uncorrectable error, and to selectively initiate an error recovery process for the prefetched data packet if the prefetched data packet is transferred to the request module and the identifier is associated with the prefetched data packet.

7. (Canceled)

8. (Currently amended) The apparatus of claim [[7]]6, wherein the data transfer interface is configured to signal an interrupt to initiate the error recovery process in response to the identifier.

9. (Currently amended) The apparatus of claim [[7]]6, wherein the identifier is stored in the second location with the prefetched data packet.

10. (Original) The apparatus of claim 6, wherein the data transfer interface is further configured to store an address of the prefetched data packet within the first location.

11. (Currently amended) A system for managing errors in prefetched data, comprising:

a memory interface module configured to prefetch a data packet from a memory array to a temporary buffer in anticipation of receiving a request for the prefetched data packet and prior to receiving the request;

a validation module in communication with the memory interface module, the validation module configured to determine whether the prefetched data packet contains an uncorrectable error;

an identification module configured to associate an identifier with the prefetched data packet prior to receiving the request if the prefetched data packet contains the uncorrectable error;

a communication module in communication with the temporary buffer, the communication module configured to transmit the prefetched data packet from the temporary buffer across a communication bus to a requesting device if the request is received; and

an error recovery module in communication with the communication module, the error recovery module configured to selectively initiate an error recovery process for the prefetched data packet if the identifier is associated with the prefetched data packet ~~that contains an uncorrectable error~~ and the prefetched data packet has been transmitted by the communication module.

12. (Canceled)

13. (Currently amended) The system of claim 1[[2]]1, wherein the identifier is stored in the temporary buffer with the prefetched data packet that contains an uncorrectable error.

14. (Original) The system of claim 11, wherein the validation module is further configured to store an address that contains an uncorrectable error within the memory array of the prefetched data.

15. (Original) The system of claim 11, wherein the error recovery module is further configured to set a flag in response to transmission of prefetched data that contains an uncorrectable error, and wherein the validation module is configured to signal an interrupt to initiate an error recovery process in response to the flag.

16. (Currently amended) A method for managing errors in prefetched data, the method comprising:

prefetching a data packet from a first location into a second location in anticipation of receiving a request for the prefetched data packet and prior to receiving the request;

determining that [[a]]the prefetched data packet contains at least one uncorrectable error;

associating an identifier with the prefetched data packet prior to receiving the request if the prefetched data packet contains the at least one uncorrectable error;

determining that the prefetched data packet in the second location has been transmitted for an intended use in response to the request; and

selectively initiating an error recovery process only for the prefetched data packet if the identifier is associated with the prefetched data packet ~~that contains~~

~~at least one uncorrectable error~~ and the prefetched data packet has been transmitted for an intended use.

17. (Canceled)

18. (Currently amended) The method of claim 1[[7]]6, further comprising signaling an interrupt to initiate the error recovery process in response to the identifier for the prefetched data packet.

19. (Currently amended) The method of claim 1[[7]]6, further comprising storing the identifier in the second location with the prefetched data packet.

20. (Original) The method of claim 16, further comprising storing an address for the prefetched data packet within the first location.

21. (Original) The method of claim 16, further comprising:

setting a flag in response to transmission of the prefetched data packet; and

interrupting a data transfer of prefetched data in response to the flag.

22. (Currently amended) An apparatus for managing errors in prefetched data, comprising:

means for prefetching a data packet from a first location into a second location in anticipation of receiving a request for the prefetched data packet and prior to receiving the request;

means for determining that [[a]]the prefetched data packet contains at least one uncorrectable error;

means for associating an identifier with the prefetched data packet prior to receiving the request if the prefetched data packet contains the at least one uncorrectable error;

means for determining that the prefetched data packet in the second location has been transmitted for an intended use in response to the request; and

means for selectively initiating an error recovery process only for the prefetched data packet if the identifier is associated with the prefetched data packet ~~that contains at least one uncorrectable error~~ and the prefetched data packet has been transmitted for an intended use.

23. (Canceled)

24. (Currently amended) The apparatus of claim 2[[3]]2, further comprising means for signaling an interrupt to initiate the error recovery process in response to the identifier for the prefetched data packet.

25. (Currently amended) The apparatus of claim 2[[3]]2, further comprising means for storing the identifier in the second location with the prefetched data packet.

26. (Original) The apparatus of claim 22, further comprising means for storing an address within the first location for the prefetched data packet.

27. (Currently amended) An article of manufacture comprising a program storage medium readable by a processor and embodying one or more instructions executable by a processor to perform a method for managing errors in prefetched data, the method comprising:

prefetching a data packet from a first location into a second location in anticipation of receiving a request for the prefetched data packet and prior to receiving the request;

determining that [[a]]the prefetched data packet contains at least one uncorrectable error;

associating an identifier with the prefetched data packet prior to receiving the request if the prefetched data packet contains the at least one uncorrectable error;

determining that the prefetched data packet in the second location has been transmitted for an intended use in response to the request; and

selectively initiating an error recovery process for the prefetched data packet if the identifier is associated with the prefetched data packet ~~that has been determined to contain at least one uncorrectable error~~ and the prefetched data packet has been transmitted for an intended use.

28. (Canceled)

29. (Currently amended) The article of manufacture of claim 2[[8]]7, wherein the method further comprises signaling an interrupt to initiate the error recovery process in response to the identifier for the prefetched data packet.

30. (Original) The article of manufacture of claim 29, the method further comprising storing an address for the prefetched data packet within the first location.

31. (New) The apparatus of claim 1, wherein the error recovery process comprises re-retrieving the data packet from the first location.

32. (New) The apparatus of claim 6, wherein the error recovery process comprises re-retrieving the data packet from the first location.

33. (New) The system of claim 11, wherein the error recovery process comprises re-retrieving the data packet from the memory array.

34. (New) The method of claim 16, wherein the error recovery process comprises re-retrieving the data packet from the first location.

35. (New) The apparatus of claim 22, wherein the error recovery process comprises re-retrieving the data packet from the first location.

36. (New) The article of manufacture of claim 27, wherein the error recovery process comprises re-retrieving the data packet from the first location.